

This Page Is Inserted by IFW Operations
and is not a part of the Official Record

BEST AVAILABLE IMAGES

Defective images within this document are accurate representations of the original documents submitted by the applicant.

Defects in the images may include (but are not limited to):

- BLACK BORDERS
- TEXT CUT OFF AT TOP, BOTTOM OR SIDES
- FADED TEXT
- ILLEGIBLE TEXT
- SKEWED/SLANTED IMAGES
- COLORED PHOTOS
- BLACK OR VERY BLACK AND WHITE DARK PHOTOS
- GRAY SCALE DOCUMENTS

IMAGES ARE BEST AVAILABLE COPY.

**As rescanning documents *will not* correct images,
please do not report the images to the
Image Problem Mailbox.**

20-MAG-2004 12:32

DA S.I.B. VARESE 0332282458

A: 00014078412343

P:2/7

20-MAG-2004 11:11

PATENT & TRADEMARK OFFICE

P.04

Inv. da: 8TMICROELECTRONICS;

0957404014 ;

20-Mag-04 11:40;

Page 4

DECLARATION AND POWER OF ATTORNEY FOR PATENT APPLICATION

Attorney Docket No.: 02CT39253415

As a below named inventor, I hereby declare that:

My residence, mailing address and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled: METHOD OF PERFORMING A SIMON'S OR A SHOR'S QUANTUM ALGORITHM AND RELATIVE QUANTUM GATE, the specification of which:

(check one)

_____ is attached hereto

X was filed on December 15, 2003

as Application Serial No. 10/736,237

and was amended on _____ (if applicable)

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulation, 1.56(u).

I hereby claim foreign priority benefits under Title 35, United States Code, 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Inv. of: 8T/MICROELECTRONICS;

Prior Foreign Application(s) Priority Claimed

<u>VA2002A0000069</u>	<u>IT</u>	<u>13 December 2002</u>	<input checked="" type="checkbox"/> [X]	<input type="checkbox"/> []
(Number)	(Country)	(Day/Month/Year Filed)	Yes	No
<u> </u>	<u> </u>	<u> </u>	<input type="checkbox"/> []	<input type="checkbox"/> []
(Number)	(Country)	(Day/Month/Year Filed)	Yes	No
<u> </u>	<u> </u>	<u> </u>	<input type="checkbox"/> []	<input type="checkbox"/> []
(Number)	(Country)	(Day/Month/Year Filed)	Yes	No

I hereby claim the benefit under Title 35, United States Code, 120, of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, 1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

<u> </u>	<u> </u>	<u> </u>
(Appln Serial No.)	(Filing Date)	(Status)
(patented, pending, aban.)		

<u> </u>	<u> </u>	<u> </u>
(Appln Serial No.)	(Filing Date)	(Status)
(patented, pending, aban.)		

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

20-MAG-2004 12:33 DA: S.I.B. VARESE 0332282458

A: 00014078412343

P:4/7

20-MAG-2004 11:41

P. 06

Pagina 8/8

20-Mag-04 11:40;

0957404014 ;

Inv. da: BTNICOELECTRONICS;

English Language Declaration

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorneys and/or agents to prosecute this application and transact all business in the Patent and Trademark Office connected therewith: Christopher F. Regan, Reg. No. 34,906; Herbert L. Allen, Reg. No. 25,322; David L. Sigalow, Reg. No. 36,006; Richard K. Warther, Reg. No. 32,180; Michael W. Taylor, Reg. No. 43,182; Henry Estavez, Reg. No. 37,823; Paul J. Ditzner, Reg. No. 40,455; John F. Woodson, II, Reg. No. 45,236; Charles E. Wands, Reg. No. 25,649; Jacqueline E. Hartt, Reg. No. 37,845; Carl Napolitano, Reg. No. 37,405; Jon M. Gibbs, Reg. No. 47,594; David L. Stewart, Reg. No. 37,578 and Lisa K. Jorgenson, Reg. No. 34,845.

Send Correspondence to:

CHRISTOPHER F. REGAN, ESQUIRE
ALLEN, DYER, DOFFELT, MILLER & GILCHRIST, P.A.
P.O. Box 3791
Orlando, Florida 32802-3791

Direct Telephone Calls to:

Christopher F. Regan
(407) 841-2330

Full name of first inventor: Antonino CALABRO

Inventor's Signature: X. Antonino Calabro Date: 19-05-2004

Residence: Villa San Giovanni, Italy

Citizenship: Citizen of Italy

Mailing Address: Via Crispo, 13
89018 Villa San Giovanni, Italy

Received at: 7:00AM, 5/20/2004

20-MAG-2004 12:33 DA: S.I.B. VARESE 0332282458

A: 00014078412343

P:5/7

20-MAG-2004 11:41

P. 07

Inv. da: STMICROELECTRONICS;

0957404014 ;

20-Mag-04 11:41;

Pagina 1/1

Full name of second inventor: **Domenico PORTO**

Inventor's
Signature: *Domenico Porto*

Date: 13-5-2004

Residence: **Catania, Italy**

Citizenship: **Citizen of Italy**

Mailing Address: **Via Orchidea, 3
95123 Catania, Italy**

-4-